



Serial No.: 09/295,431

Inventors: Yoshimura et al.

Examining Attorney: Michael P. Mooney

Attorney Docket No.: 25916-118 (6136-53461)

Confirmation No.: 7173

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of

Yoshimura et al.

For: **MULTI-LAYER OPTO-ELECTRONIC  
SUBSTRATES WITH ELECTRICAL AND  
OPTICAL INTERCONNECTIONS AND  
METHODS FOR MAKING**

Serial No.: 09/295,431

Filed: April 20, 1999

Examining  
Attorney: Michael P. Mooney

Attorney  
Docket No.: 25916-118 (6136/53461)

Assistant Commissioner for Patents  
Washington, D.C. 20231

Dear Sir:

**CERTIFICATE OF EXPRESS MAIL  
Under 37 C.F.R. § 1.10**

I hereby certify that this paper or fee is being deposited with the United States Postal Service "Express Mail" Service on the date indicated below and is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

June 18, 2002  
Date

  
Shawn Anderson

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**RESPONSE TO OFFICE ACTION**

In response to the Office Action mailed on April 19, 2002 on the above referenced application, applicant elects Group 1 (claims 1-18) without traverse.

Applicant requests the following amendments be entered prior to examination:

**IN THE CLAIMS:**

Please cancel claims 19-40.

Please replace Claims 1 and 4 with the following:

1. A substrate having optical and electrical interconnection, comprising:  
a first layer having a first polymeric waveguide formed therein;  
a second layer having a second polymeric waveguide formed therein;